

Our expertise with ALTERA products and design tools will benefit your design team for design success and reduced time-to-market.

In addition to design services provided to our clients as ALTERA Certified Design Center, we offer high quality training. Also, ALTERA selected ReFLEX Consulting as member of ALTERA Training Partner Program.



- ▶ Training provided by experts who are also actual designers,
- ▶ Certified expertise on up-to-date tools and latest technologies thanks to ALTERA Technical Training Program,
- ▶ Training programs including workshop labs,
- ▶ Comprehensive and user friendly materials provided,
- ▶ Course location :
 - ❖ Paris office for standard training,
 - ❖ on your site for personalized contents and custom program
- ▶ Wide Range of Standard Programs available :
 - ❖ ALTERA design flow,
 - ❖ ALTERA Design Optimization,
 - ❖ VHDL : introduction to expertise level,
 - ❖ NIOS & NIOS II embedded processors,
 - ❖ PCI Bus fundamentals,
 - ❖ PCI cores overview



Contacts



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ALTERA Design Flow – 2 days

- ▶ ALTERA device architecture
- ▶ Methodology overview
- ▶ Familiarization with simulation tool (ModelSim)
- ▶ Familiarization with QUARTUS II
 - Quartus II Overview
 - Design Optimization
- ▶ Workshop labs

Introduction to VHDL – 3 days

- ▶ VHDL language concept
- ▶ Overview of coding rules : behavioral & RTL
- ▶ RTL coding styles for efficient design
 - Syntax rules
 - Coding rules
 - Synthesis rules
 - FPGA design guidelines
- ▶ Behavioral coding styles for test benches
- ▶ Workshop labs

PCI Cores Overview – 1 day

- ▶ Features
- ▶ Instantiation
- ▶ Architecture
- ▶ Functional modules operation
- ▶ Local interface description
- ▶ PCI test bench overview
- ▶ FPGA-based design flow

Advanced ALTERA – 1 days

- ▶ LogicLock & Performance Optimization Techniques
- ▶ Debugging Design Using SignalTap II Logic Analysis
- ▶ Using IP & Optimizing Design
- ▶ Implementing DSP Design
- ▶ Workshop labs

NIOS II – 1 days

- ▶ NIOS II Overview :
 - Hardware Development
 - Software Development & Debug
- ▶ RTL Simulation
- ▶ User Peripherals
- ▶ Custom Instructions
- ▶ NIOS II Software Studio
- ▶ Workshop labs

PCI Bus Fundamentals – 2 days

- ▶ PCI local bus architecture
- ▶ Signals description / Bus operation
- ▶ Addressing & Bus commands
- ▶ PCI Configuration / Protocol
- ▶ Electrical / Timing / Mechanical specifications
- ▶ 66 MHz specifications
- ▶ System-level features, performance & optimizations
- ▶ PCI software & derivate standards

Standard Course Schedule in 2005

	Introduction to VHDL	ALTERA Design Flow	Advanced ALTERA	NIOS II	PCI Cores Overview & Bus Fundamentals
Jan 05	10-11-12	13-14	17	18	24-25-26
Feb 05	1-2-3	7-8	9	10	14-15-16
Mar 05	1-2-3	7-8	9	10	14-15-16
Apr 05	4-5-6	11-12	13	14	18-19-20
May 05	9-10-11	17-18	19	20	23-24-25
Jun 05	6-7-8	13-14	15	16	20-21-22
Jul 05	4-5-6	18-19	20	21	25-26-27
Aug 05					
Sep 05	5-6-7	12-13	14	20-21	19-20-21
Oct 05	3-4-5	10-11	12	13-14	17-18-19
Nov 05	14-15-16	21-22	23	24-25	28-29-30
Dec 05	5-6-7	12-13	14	15-16	

For course details, pricing information and booking, please call our Sales Team on one of the numbers below or email info_rxc@reflexconsulting.com

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